Docket No.: 67161-149 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Yasumasa TSUKAMOTO, et al. : Confirmation Number:

Serial No.: : Group Art Unit:

Filed: March 30, 2004 : Examiner:

For: SEMICONDUCTOR MEMORY DEVICE

## **INFORMATION DISCLOSURE STATEMENT**

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In a ccordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of the six references listed on attached Form 1449 is discussed in the present specification.

## Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Date: March 30, 2004

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION						ATTY. DOCKET NO. SERIAL NO. 67161-149					
						APPLICANT Yasumasa TSUKAMOTO, et al.					
(PTO-1449)						FILING DATE GROUP March 30, 2004					
				U.S. PATE	NT	DOCUMENTS					
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		-	44.040070	20/20/1002					Yes	No	
		_	11-213673	08/06/1999		MATSUSHITA ELECTRIC IND CO LTD			Japanese (w/ English Abstract)		
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	A .	P2003-60089		02/28/2003		MITSUBISHI ELECTRIC CORP			Japanese (w/ English Abstract)		
	A	<u> </u>	P2001-339071	12/07/2001		MITSUBISHI ELECTRIC CORP			Japanese (w/ English Abstract)		
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Hiroshi KAWAGUCHI et al., "DYNAMIC LEAKAGE CUT-OFF SCHEME FOR LOW-VOLTAGE SRAM's", IEEE 1998 Symposium on VLSI Circuits Digest of Technical Papers, pp. 140-141											
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.